

DA 931/21
#603
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



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C. Magallón
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Applicant : Ahmadreza Rofougaran, et al.
Application No. : 09/698,498
Filed : October 27, 2000
Title : ADAPTIVE RADIO TRANSCEIVER WITH CMOS
OFFSET PLL
Grp./Div. : 2682
Examiner : To be assigned
Docket No. : 40886/CAG/B600

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SUPPLEMENTAL PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Post Office Box 7068
Pasadena, CA 91109-7068
August 23, 2001

Commissioner:

Before examination please amend the above-identified application, as follows:

In the Claims:

Please cancel Claims 22-28 without prejudice. Claims 1-21 remain unchanged and are included herein for reference purposes.

1. (Unchanged) A complimentary metal oxide semiconductor (CMOS) phase lock loop, comprising:
an oscillator having a tuning input, and an output with a tunable frequency responsive to the tuning input;